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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Art Unit:

Not Assigned

Tzueng-yau Lin

Examiner: Not Assigned

Serial No: Not Assigned

Filed: Herewith

For: DVD AUDIO ENCODER AND DECODER

TRANSMITTAL OF FORMAL DRAWINGS

BOX PATENT APPLICATION

Attn: OFFICIAL DRAFTSPERSON

Commissioner for Patents Washington, D.C. 20231

Dear Sir:

Enclosed are the formal drawings (7 sheets – Figures 1-6) in the above-captioned case.

Respectfully submitted,

HOGAN & HARTSON L.I.

Date: May 7, 2001

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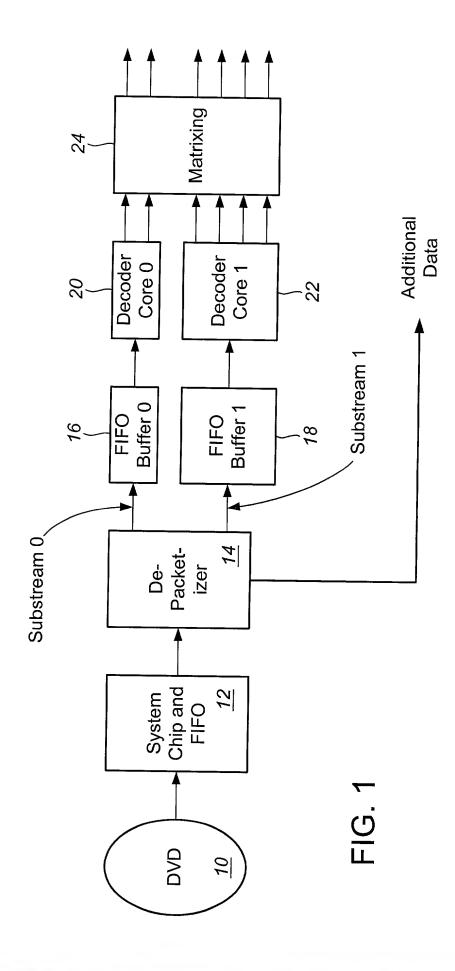
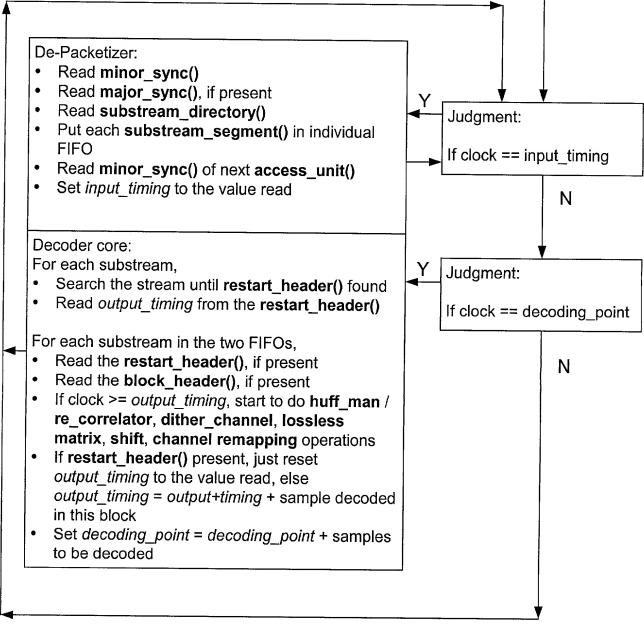
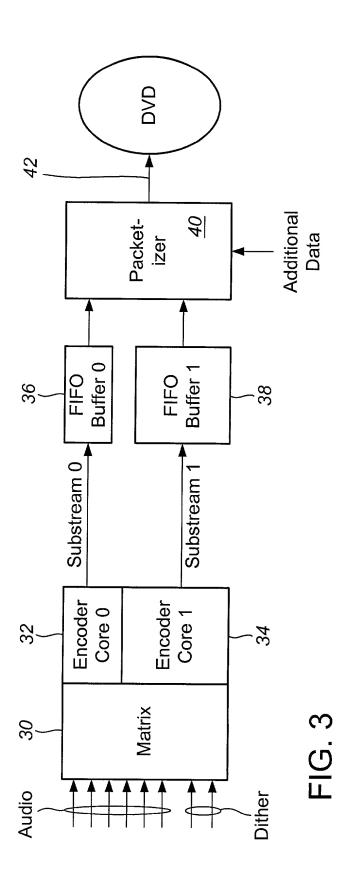


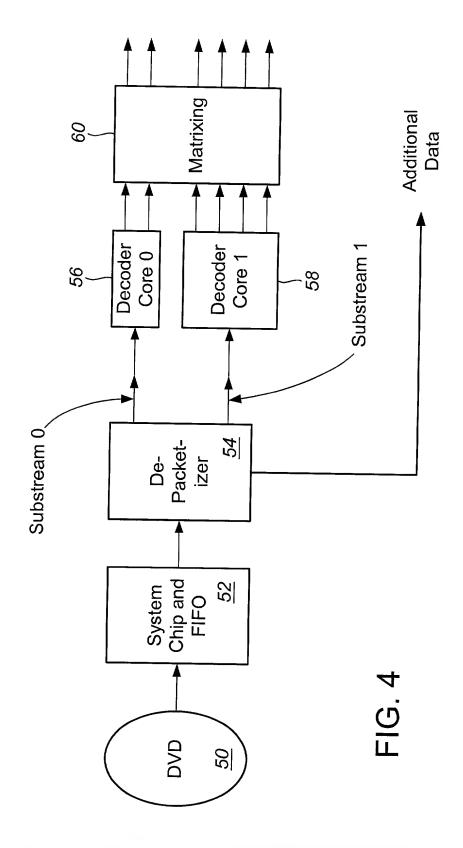
FIG. 2

Initialization: Find major_sync() Read back to find input timing from minor_sync() Set clock to input timing Set decoding_point to (clock + sample to be decoded)



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Initialization:

- Search major_sync()
- Read back to read parameters from minor_sync() and major_sync() (discard input_timing)
- Read substream_directory()
- Read the substream_segment() until restart_header is found
- Read the output_timing and set the clock = output timing

FIG. 5A

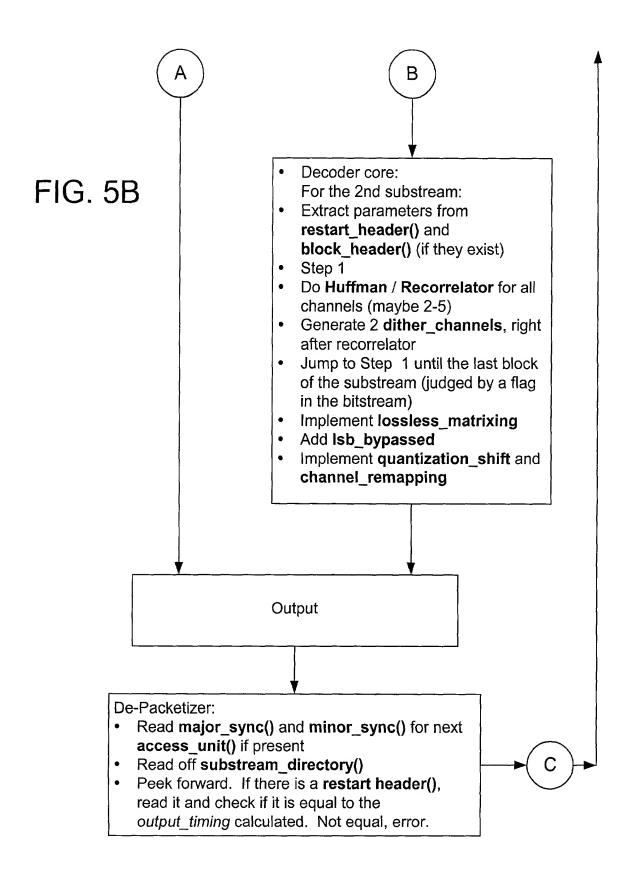
Judgment:
One or Two substreams?

- Decoder core:
- Extract parameters from restart_header() and block header() (if they exist)
- Step 1
- Do Huffman / Recorrelator for all channels (maybe 0-5)
- Generate 2 dither_channels, right after recorrelator
- Set output_timing = output_timing + block size
- Jump to Step 1 until the last block of the substream (judged by a flag in the bitstream)
- Implement lossless_matrixing
- Add isb bypassed
- Implement quantization_shift and channel_remapping

- Decoder core:
- For the 1st substream:
- Extract parameters from restart_header() and block header() (if they exist)
- Step 1
- Do Huffman / Recorrelator for all channels (maybe 0-1) in this substream
- Set output_timing = output_timing + block size
- Jump to Step 1 until the last block of the substream (judged by a flag in the bitstream)



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